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## **CLAIMS**

## What is claimed is:

1. A computer system comprising:

a processor;

machine readable storage media for storing programs performable by the processor;

input means for receiving input from a user;

a display device for providing visual output from the software applications to the user;

a system bus connecting the processor to the display device and the input means;

an interface adapter for transferring input from the user at the input means to the system bus;

a security chip requiring a personal identifier code from the user for performance of at least one of the programs in the storage media; and

interposer means for routing the personal identifier code from the input means to the security chip independently of the system bus.

- 2. The computer system of claim 1, wherein the processor are located on a motherboard in the computer system.
- 3. The computer system of claim 2, wherein the interposer means and the security chip are located separately from the motherboard in the computer system.
- 4. The computer system of claim 2, wherein the interposer means and the security chip are located separately on a separate card from the motherboard in the computer system.

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- 5. The computer system of claim 2, wherein the interposer means is located separately on a separate board from the motherboard in the computer system.
- 6. The computer system of claim 1, wherein the interposer means comprises: interposer means for routing the personal identifier code directly from the input means to the security chip.
- 7. The computer system of claim 1, wherein:
  the user interface adapter connects the interposer means to the security chip.
- 8. The computer system of claim 1, wherein the interposer means comprises: interposer means for routing the personal identifier code from the input means to the user interface adapter.
- 9. The computer system of claim 1, wherein data and clock signals are provided between the input means and the user interface adapter and the interposer means further comprises:

means for blocking the data and clock signals between the input means and the user interface adapter.

- 10. The computer system of claim 1, wherein the input means comprises a keyboard for entry of the personal identifier code.
- 11. The computer system of claim 1, wherein the input means comprises:
  a keyboard for entry of data; and
  a keypad for entry of the personal identifier code.
- 12. The computer system of claim 1, wherein the input means comprises: a keyboard for entry of data; and

3	a fingerprint reader for scanning a user fingerprint to verify an authorized		
4	fingerprint as the personal identifier code.		
5	13. The computer system of claim 1, wherein the input means comprises:		
6	a keyboard for entry of data; and		
7	a card reader for scanning a user card to verify an authorized card as the		
8	personal identifier code.		
1	14. A computer system comprising:		
2	a processor;		
3 PÅ	machine readable storage media for storing programs performable by the		
4	processor;		
5 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3	input means for receiving input from a user;		
	a display device for providing visual output from the software applications		
7	the user;		
8 <sub>2</sub>	a system bus connecting the processor to the display device and the input		
9	means;		
10	an interface adapter for transferring input from the user at the input means		
11	the system bus		
12	a security chip requiring a personal identifier code from the user for		
13	performance of at least one of the programs in the storage media;		
14	a direct communication channel for transferring user input from the input		
15	means to the security chip; and		
16	interposer means for routing the personal identifier code over the direct		
17	communication channel to the security chip.		
1	15. The computer system of claim 14, wherein the processor are located on a		

ring programs performable by the ıser; tput from the software applications to o the display device and the input ut from the user at the input means to tifier code from the user for the storage media; sferring user input from the input al identifier code over the direct ein the processor are located on a motherboard in the computer system.

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- 16. The computer system of claim 15, wherein the interposer means and the security chip are located separately from the motherboard in the computer system.
- 17. The computer system of claim 16, wherein the interposer means and the security chip are located separately on a separate card from the motherboard in the computer system.
- 18. The computer system of claim 16, wherein the interposer means is located separately on a separate board from the motherboard in the computer system.
- 19. The computer system of claim 14, wherein data and clock signals are provided between the input means and the user interface adapter and the interposer means further comprises:

means for blocking the data and clock signals between the input means and the user interface adapter.

- 20. The computer system of claim 14, wherein the input means comprises a keyboard for entry of the personal identifier code.
- 21. The computer system of claim 14, wherein the input means comprises: a keyboard for entry of data; and a keypad for entry of the personal identifier code.
- 22. The computer system of claim 14, wherein the input means comprises: a keyboard for entry of data; and
- a fingerprint reader for scanning a user fingerprint to verify an authorized fingerprint as the personal identifier code.
- 23. The computer system of claim 14, wherein the input means comprises:

7		a card reader for scanning a user card to verify an authorized card as the		
8	personal identifier code.			
1	24.	A method of transferring a personal identifier code to a security chip in a		
2		nal computer system comprising the steps of:		
3	F	entering the personal identifier code in a security entry input to the personal		
4	compi	uter in response to a request from a processor of the computer over a computer		
5	system bus;			
6		receiving the personal identifier code in an interposer connected between the		
7	security entry input and the security chip; and			
€.d 8€35		transferring the personal identifier code from the interposer independently of		
7 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 -	the co	the computer system bus.		
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1 <sup>%</sup> 續	25.	The method of claim 24, wherein the security entry input comprises a		
2 <sub>8</sub>	keybo	ard for entry of the personal identifier code.		
2 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4				
1	26.	The method of claim 24, wherein the security entry input comprises:		
2		a keyboard for entry of data; and		
3		a keypad for entry of the personal identifier code.		
1	27.	The method of claim 24, wherein the security entry input comprises:		
2		a keyboard for entry of data; and		
3		a fingerprint reader for scanning a user fingerprint to verify an authorized		

fingerprint as the personal identifier code.

a keyboard for entry of data; and

The method of claim 24, wherein the security entry input comprises: 28. a keyboard for entry of data; and

a card reader for scanning a user card to verify an authorized card as the personal identifier code.

## 29. The method of claim 24, wherein:

the step of transferring comprises transferring the personal identifier code over a direct communication channel from the security entry input to the security chip.